

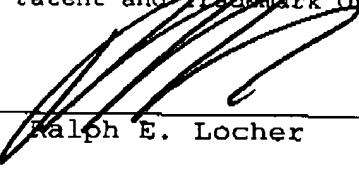
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By:


Ralph E. Locher

Date:

March 21, 2007IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/730,619 Conf. No.: 4277
Applicant : Burkhard Becker
Filed : December 8, 2003
Art Unit : 2185
Examiner : Yong Choe
Title : Method and Circuit Configuration for
Transmitting Data Between a Processor
and a Hardware Arithmetic-Logic Unit
Docket No. : L&L-I0225
Customer No. : 24131

R E S P O N S E T O O F F I C E A C T I O N

Mail Stop Non Fee Amendment
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P.O. Box 1450
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Sir:

Responsive to the Office Action dated December 21, 2006
kindly consider the following remarks:

Remarks/Arguments begin on page 2 of this paper.